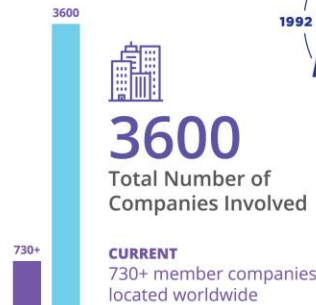
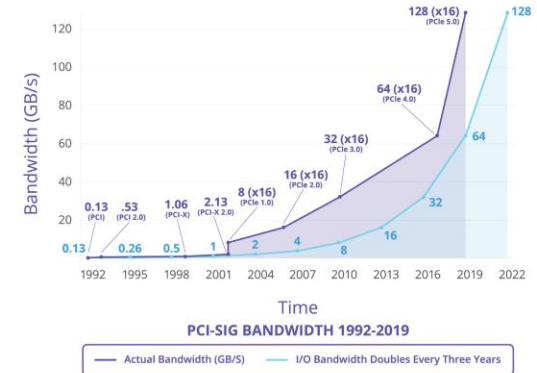


HISTORY OF PCI-SIG®



40%
Percentage of enterprise SSD revenue expected from PCIe-based SSD sales by 2019

PCI-SIG History I/O BANDWIDTH DOUBLES Every 3 Years



Where in the world is PCI-SIG?

177
Total Events



30 Major Specifications Published

PCI EXPRESS®

- PCI Express Base Specification
- PCI Express Card Electromechanical Specification
- PCI Express M.2 Specification
- PCI Code and ID Assignment Specification
- PCI Express Connector High Speed Electrical
- PCI Express OCuLink Specification
- PCI Express Architecture Mobile Graphics Lo-Power Addendum to the PCI Express Base Specification
- Multi-Root I/O Virtualization and Sharing Specification
- Single Root I/O Virtualization and Sharing Specification
- Address Translation Services
- PCI Express External Cabling Specification
- PCI Express ExpressModule Electromechanical Specification
- PCI Express x16 Graphics 150W-ATX Specification
- PCI Express to PCI/PCI-X Bridge Specification
- PCI Express Mini Card Electromechanical Specification
- PCI Express 225 W/300 W High Power Card Electromechanical

CONVENTIONAL PCI

- PCI Local Bus
- PCI Firmware Specification
- Mini PCI Specification 1.0
- PCI Bios Specification Revision 2.1
- PCI Standard Hot-Plug Controller and Subsystem Specification
- PCI Hot-Plug Specification
- PCI-to-PCI Bridge Architecture Specification

PCI-X

- PCI-X 2.0 Electrical Report
- PCI-X Electrical and Mechanical Addendum to the PCI Local Bus Specification
- PCI-X Protocol Addendum to the PCI Local Bus Specification

PCI EXPRESS® - TEST SPECIFICATIONS

- PCI Express Architecture Platform Init / Config
- PCI Express Architecture Link Layer and Transaction Layer
- PCI Express Architecture Configuration Space
- PCI Express Architecture PHY